

Sheet 1 of 1

**INFORMATION DISCLOSURE CITATION
IN AN APPLICATION**

(Use several sheets if necessary)

Attorney Docket No.:
02P18560

Application No.:
NEW NONPROVISIONAL

Applicant:
Paolo DeANNA et al.

Filing Date:
December 10, 2003

Group Art Unit:

U.S. PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing date (if appropriate)

FOREIGN PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation	
						Yes	No
AB	EP 0 798 857	10/1/1997	EUROPE			*	

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

AB		MOHAN et al., "dc-dc Switch-Mode Converters" and "Switching dc Power Supplies", POWER ELECTRONICS, John Wiley & Sons, 1995, New York, Chapters 7 and 10
		Philip C. TODD, "Snubber Circuits: Theory, Design and Application", POWER SUPPLY DESIGN SEMINAR MANUAL, Unitrode, Merrimack, New Hampshire, 1993, pages 2-1 - 2-17

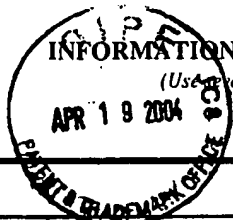
EXAMINER:

A. Berlane

DATE CONSIDERED

12 / 04

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.



INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)

Docket Number (Optional)

02P18560

Application Number

10/730,931

Applicant(s)

Paolo De Anna, et al.

Filing Date

12/10/2003

Group Art Unit

2838

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
AB		5,703,763 A	12/30/1997	Smeets	363	20	08/29/1996
AB		5,880,943 A	03/09/1999	Yokoyama	363	56	04/22/1997
AB		2002/176262 A1	11/28/2002	Tripathi, et al.	363	21.15	05/25/2001

FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

AB	(1) Duarte, C.M.C. et al., "Family of ZVS-PMW Active-Clamping DC-to-DC Converters: Synthesis, Analysis, Design, and Experimentation," <i>IEEE Transactions on Circuits and Systems I: Fundamental Theory and Applications</i> , IEEE Inc., New York, U.S., Vol. 44, No. 8, August 1997, pp. 698-703.
AB	(2) <i>Patent Abstracts of Japan</i> , Vol. 1999, No. 4 (April 30, 1999), referencing Japanese Patent Publication No. 11008978 (published January 12, 1999).
	(3) European Patent Office Search Report (referencing all documents listed above) dated 02/10/2004.

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